

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Cohen et al.

Serial No.: 09/612,260

Filing Date: July 7, 2000

For: SELF-ALIGNED DOUBLE GATE MOSFET WITH SEPARATE GATES

Commissioner of Patents  
P.O. BOX 1450  
Alexandria, VA 22313-1450

Group Art Unit: 2811

Examiner: D. Kang

## INFORMATION DISCLOSURE STATEMENT

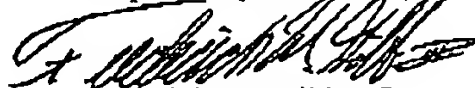
Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0510.

Respectfully submitted,



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PAGE 3/4 \* RCVD AT 6/25/2004 9:49:25 AM [Eastern Daylight Time] \* SVR:USPTO-EFXXRF-1/0 \* DNIS:8729306 \* CSID:3012618825 \* DURATION (mm-ss):01-24